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Dkt. No. 2271/66674

**Listing of Claims**

The following listing of claims will replace all prior versions, and listings, of claims in the subject application:

1. (previously presented) A bus arbitration apparatus, comprising:

a storage arranged and configured to store a plurality of selection signals for specifying a priority order against a number N of requests for use of a bus;

a priority order determiner arranged and configured to cause said storage to output one of said plurality of selection signals in a predetermined sequence in response to a demand for arbitration; and

an arbitrator arranged and configured to perform an arbitration operation based on said priority order against said number N of requests specified by said one of said plurality of selection signals which is output from said storage.

2. (original) A bus arbitration apparatus as defined in Claim 1, wherein said storage stores a number N or more of said selection signals for differently specifying said priority order against said number N of requests.

3. (original) A bus arbitration apparatus as defined in Claim 1, wherein said storage is a re-programmable storage.

4. (original) A bus arbitration apparatus as defined in Claim 3, wherein said storage comprises:

a plurality of re-programmable registers for storing said plurality of selection signals; and

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